CONTROLLERS FOR FAULT TOLERANT ACTUATORS

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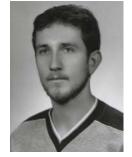
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Abstract. The Fly-by-Wire technology enables flexible shaping of both effective dynamics and handling qualities of aircraft. This solution extends aircraft possibilities and supports the pilot by use of high-level control laws. Application of FBW for aircraft would make flying both safer, and more popular. On the other hand the FBW system must be highly reliable. Advanced indirect flight control technology requires the use of actuators characterized by fault tolerant architecture. It should enable improvement of reliability of the aircraft control system and safety of performed flights effectively. The Electromechanical Actuator (EMA) consists of the electric motor, gearbox and controller. The actuator controller should be made intrinsically reliable and should be insensitive to other equipment failure. The conception of fault tolerant control algorithms as well as practical realization of it has been presented in this work. Tests of reliability of the complete EMA unit have also been presented herein.

Keywords: actuator, controller, Power-by-Wire, Fly-by-Wire.

Introduction

Fly-by-wire (FBW) systems have been applied since the end of the 1960s. In the beginning they were analogy solutions. They allowed aircraft to overcome problems with conventional systems (for example Concorde), or they were experimental installations [11]. The first digital FBW was used on the F-8 Crusader aircraft and then in the space shuttle program, where it was a sheer necessity as task performance would have been impossible with conventional mechanically augmented configurations. This technique allows an extension of possibilities for aircraft control, stabilization of flight parameters and enables better flexible automatic control. Indirect control

of aircraft improves flight safety and minimizes pilot load. The development of digital technology makes FBW systems more profitable and the next step seems to be using these systems in general aviation aircrafts [3, 12, 4]. Indirect control of aircraft improves flight safety and minimizes pilot load. The pilot can work as computer operator and also control the plane classically by using the control wheel or side stick in standard mode [6]. For this kind of control, actuators are responsible for movements of aircraft control surfaces. For these aircrafts in particular, actuators are critical elements of the FBW control systems.

The conception of fault tolerant actuators

The electromechanical actuator (EMA) realized in power-by-wire (PBW) technology seems to be the next major step in the development of aircraft control. In this solution, control power comes directly from the aircraft's electrical system. Removing hydraulic systems would greatly reduce the amount of support equipment and maintenance costs [5]. On the other hand actuators are critical elements of the FBW system and they should perform strict criteria. They have to be reliable and work stably in every situation. It is very important because an actuator failure can cause serious accidents. The control surface actuator should be precise, fast, powerful and reliable [2]. An important part of this element is the electronic controller. Reliability of the EMA can be improved by using the appropriate hardware structure of micro controller, and also by using adequate control techniques and software solutions.

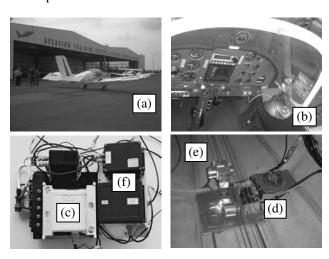


Fig 1. General view of PZL-110 "Koliber" aircraft (a), cockpit (b), main parts of SPS-1 system: flight control computer (c), controller (d), electromechanical unit (e), measurement equipment (f)

Data transmission network

The actuator should be intrinsically reliable and insensitive to other equipment failures. This second feature can be achieved by multiplication of superior control circuits: data buses, flight computers and measurement equipment. The structure and pictures of the SPS-1 indirect flight control system are presented in figures 1-2. This system has been designed at the Department of Avionics and Control, Rzeszów University of Technology. The data transmission network of SPS-1 is based on the serial communication bus CAN2.0 [9]. The CAN standard was developed in the 1980s for the automotive industry [1]. Since then, this standard has been disseminated and is used in many fields of technology. It defines hardware and data transmission methods as well as ways of solving conflicts and bus failure detection. Units and tools necessary for its service are easily available and this standard is characterized by

good performance. The probability of data failure in a CAN network is around 10⁻¹³ per message transmission [1]. Assuming the full load of the bus (around 8000 messages per second at high-speed 1000Mb/s), this will result in a probability of 2.9*10⁻⁶ undetected failures per flight hour [10].

The SPS-1 system CAN data buses are doubled and three flight control computers (FCCs) are connected to both. Redundant system architecture is supported by the CAN aerospace data transmission protocol [10]. This standard requires 29-bit identifiers of data frames. In order to create redundant identifiers, a constant value must be added. In CAN aerospace v1.6, the value equalled 10000 (decimal). Established CAN standard of data transmission is very reliable and fault tolerant. It also enables the performance of advanced real-time laboratory and in-flight tests of the controller. Implementation of the original CAN aerospace protocol was difficult because of the decimal value added to redundant controller (and other device) identifiers [10]. The main problems were filtering and masking of identifiers by micro controllers. Every CAN controller has a limited number of accessible identifier mask registers and practical realization of the receipt of data was very hard and even impossible in many cases. Our team proposed that the authors of CAN aerospace change the offset value to a hexadecimal value and this suggestion was accepted. The next version of protocol will be changed in this point.

Structure of EMA controller

The EMA controller is connected directly to the electrical system of the aircraft and the electrical engine is powered through a built-in power output (Fig 3). The control signals are delivered to a micro controller from three independent flight control computers (through CAN buses) and one extra line is connected directly to the sidestick (emergency control). The controller hardware is based on a microprocessor equipped with a watchdog timer, an A/D converter, three I/O ports, and a capture compare module, which is used for catching PWMs (pulse width modulation signals). The microprocessor is galvanically isolated from input and output circuits by transports. The power is supplied from a DC/DC converter. A security chip powered from the board power lines of the aircraft assures it. Feedback from deflection is realized by a potentiometer installed in the electromechanical unit and is powered by the controller circuit.

The software of the EMA controller is realized in an assembler. Low-level programming allows avoidance of possible high-level compiler errors and makes software-hardware relations clearer. Particular procedures and functions of the software were optimized and tested separately. During successive stages of software development, interconnections were also tested. The complete software was implemented on hardware and tested under CAN Monitor application supervision [9].

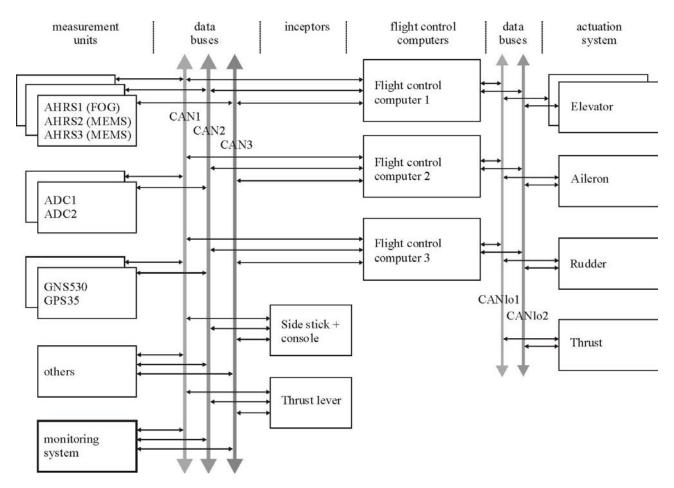


Fig 2. Block scheme of SPS-1 system and set of electromechanical actuators

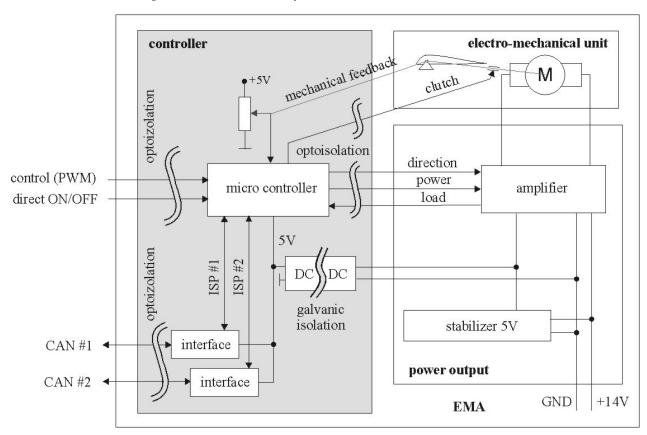


Fig 3. Scheme of EMA controller

Control algorithms

The actuator controller receives data from three independent flight control computers through two CAN data buses. From all the received signals only one control signal is selected and it is only used for positioning the control surface. The selection process is repeated equally in every cycle of controller work 20 times per second. Two fault tolerant low-speed CAN data buses are used to improve the reliability of the transmission network and, in effect, one control signal out of three received is chosen (not one from six). Doubled data frames are sent from the same flight control computer (FCC) through different lines and they refresh the same controller register. The latest one is used during the next computations.

Selection of control signals

The selection process consists of these steps. Improper values (too low, too high or unexpected) are rejected at first. Desired positions of control surfaces (received from FCCs) are verified and sorted according to the highest value. If the differences between neighbour signals are correct, a medium value signal is chosen as the desired position of the control surface. The selection algorithm generates warning and error diagnostic frames to all FCCs in case of improper values of transmitted data or in case of high differences between the received signals. Information about outdated or not received data fames is also sent to all computers. At least two verified, up to date and low differenced signals are required for the selection process. Particular cases of selection algorithms are presented in table.

Table. Particular cases of selection algorithm

S1	S2	S3	Δ_1	Δ_2	Voted
•	•	•	$\sqrt{}$	$\sqrt{}$	S2
•	•	•	$\sqrt{}$	X	S2
•	•	•	X	\checkmark	S2
•	•	•	X	X	st
•	•	0	$\sqrt{}$	-	S2
•	•	0	X	-	st
•	0	0	-	-	st
0	0	0	-	-	st

- S1-3 received and sorted according to the highest value signals from FCCs,
- ○/• absence / presence of signal,
- \sqrt{x} acceptable / non acceptable difference,
- st desired position of control surface delivered directly from pilot inceptor.

If selection between signals delivered from FCCs is impossible, all control surfaces are frozen in their last position and a pilot can deflect the controls be Ed via his side-stick (or control-wheel). The pilot can switch control into stick input at any moment during the flight by switching DIRECT to ON position. To achieve high

reliability this process is done without the use of FCCs. The control signals are transmitted directly from the sidestick to the actuator controllers through PWM (pulse width modulation) lines. The assumed standard of PWM enables the detection of a short circuit or broken external circuits. To achieve this, a periodical signal with two constant zones, "0" and "1" have been adopted. Filling the PWM input is considered as a short circuit, and a steady zero, as a break. An example of time domain analysis of voting algorithm obtained during real-time tests of the controller is presented in figures 4-6 [5].

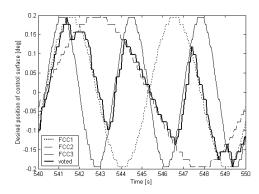


Fig 4. Test 1 of selection algorithm: differences between signals received from all *FCCs* are in acceptable range

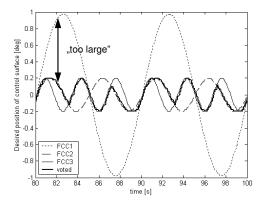


Fig 5. Test 2 of selection algorithm: signal from *FCC1* is "too high" in some periods, difference between *FCC2* and *FCC3* is acceptable – device working correctly

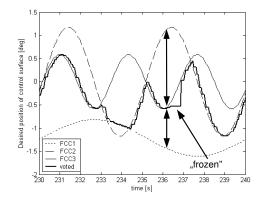


Fig 6. Test 3 of selection algorithm: both differences between signals received from *FCCs* are out of range in one period of time, control is "*frozen*" in this case

Heuristic regulator

A variable and heavy load of the actuator or frequent changes of the control signal (especially direction) can cause quick amortization of the electrical motor and the power driver. The control algorithm should prevent situations where the motor will be overloaded for a long time. After some test runs, the heuristic regulator has been adopted in the presented solution. It uses

dependences between motor powering and actuating error established by an expert. The heuristic regulator has very good static properties and good dynamic properties under rate saturation. It also allows minimization of the disadvantages of the wind-up effect to a high degree. Practical realization of this algorithm is very simple, the dependence between motor powering and actuating error can be formulated as (1)-(3):

$$y_h(k+1) = y_{h_{-}p}(k+1) + y_{h_{-}i}(k+1)$$
(1)

$$y_{h_{-p}}(k+1) = kp(e(k))$$
 (2)

$$y_{h_{-i}}(k+1) = \begin{cases} y_{h_{-i}\max}(e(k)) & if \quad y_{h_{-i}}(k) + ki(e(k)) \cdot e(k) \cdot T_d > y_{h_{-i}\max}(e(k)) \\ y_{h_{-i}\min}(e(k)) & if \quad y_{h_{-i}}(k) + ki(e(k)) \cdot e(k) \cdot T_d < y_{h_{-i}\min}(e(k)) \\ otherwise \quad y_{h_{-i}}(k) + ki(e(k)) \cdot e(k) \cdot T_d \end{cases} \tag{3}$$

T_d - sample time, k - period of time,

 $\begin{array}{lll} y_h(k) & & \text{- output signal of heuristic regulator,} \\ y_{h_p}\left(k\right) & & \text{- proportional component of output signal,} \\ y_{h_i}\left(k\right) & & \text{- integral component of output signal,} \end{array}$

e (k) - actuating error,

kp (e (k)) - expert chosen thresholds for gain in proportional path,

ki (e (k)) - expert chosen thresholds for gain in integral path,

y_{h_i_max} (e (k))- maximal value of integral component of output signal (dependent on actuation error).

Conclusion

The controller operation has been checked in many possible situations. Positive results from laboratory tests made it possible to install a complete set of servos and controllers onboard the PZL-110 "Koliber" aircraft, as components of the experimental SPS-1 system. To achieve a higher safety level in the SPS-1 system, a redundant elevator servo and controller system has been adopted. A total of 12 flight hours with inputs and responses in various flight conditions have been recorded. Tests performed in flight (July-November 2003) supported the laboratory results. The next run of advanced in-flight tests of actuators is planned for summer 2006.

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